

## **BT32 Datasheet**

Amp'ed RF Technology, Co., Ltd.

## BT32 Product Specification



10.5mm x 13.5mm

### Description

Our micro-sized Bluetooth module is the smallest form factor available providing a complete RF platform. The BT32 is designed for maximum performance in a minimal space and includes 5 general purpose and A/D IO lines, several serial interface options, and up to 1.5M bps data throughput.

The BT32 is a surface mount PCB module that provides fully embedded, ready to use Bluetooth wireless technology. The reprogrammable flash memory contains embedded firmware for serial cable replacement using the Bluetooth SPP profile. Other popular Bluetooth profiles are also available.

Customized firmware for peripheral device interaction, power optimization, security, and other proprietary features may be supported and can be ordered pre-loaded and configured.

## Features

### Bluetooth features

- FCC & Bluetooth certified
- Bluetooth v3.0
- Class 1.5 radio
- Range up to 60m LOS
- Apple iOS support
- 1.5Mbps data throughput
- 128-bit encryption security
- Multipoint capability up to 7 slaves
- FM Tx/Rx integrated radio

### Hardware configuration

- Cortex-M3 microprocessor up to 72MHz
- 256K bytes flash memory
- 48K bytes RAM memory
- UART, up to 2.25Mbps
- SPI and I2C interfaces
- 5 general purpose I/O
- 4x12-bit A/D inputs
- 1 DAC output
- 1 LPO input

### Embedded software

- Amp'edUP Bluetooth stack (SPP, iAP, OBEX)
- abSerial, AT command set
- SDK, Software Development Kit (Optional)
- BlueGuard, data encryption software (Optional)
- Mobile application software (Optional)

### Additional documentation

- BT Getting Started Guide
- abSerial User Guide
- abSerial Reference Guide
- abSerial Configuration Guide

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# 1 Software Architecture

## 1.1 Lower Layer Stack

- Full Bluetooth v3.0
- Device power modes: active, sleep and deep sleep
- Wake on Bluetooth feature optimized power consumption of host CPU
- Authentication and encryption
- Encryption key length from 8 to 128 bits
- Persistent FLASH memory for BD Address and user parameter storage
- BR and EDR ACL packets.
- SCO and eSCO (Synchronous Connection Oriented) packet support.
- Point to multipoint and scatternet support: 3 master and 7 slave links allowed (10 active links simultaneously)
- Sniff, and hold modes: fully supported to maximum allowed intervals
- Master slave switch, supported during connection and post connection
- Dedicated Inquiry Access Code, for improved inquiry scan performance
- Dynamic packet selection, channel quality driven data rate to optimize link performance
- Bluetooth test modes per Bluetooth v3.0 specification
- 802.11b/g/n co-existence: AFH
- Vendor specific HCI commands to support device configuration and certification test modes

## 1.2 Upper Layer Stack: Amp'ed UP

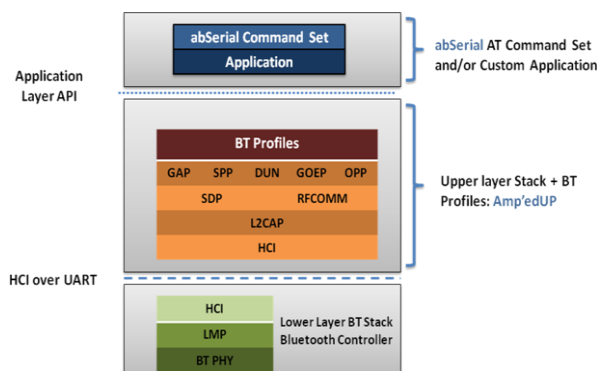
- SPP, OBEX, SDAP, GAP, and DUN protocols
- RFCOMM, SDP, and L2CAP supported
- Multipoint with 7 simultaneous slaves

## 1.3 HCI Interface

- Bluetooth v3.0 specification compliant
- HCI UART transport layer (H4)

## 1.4 AT Command Set: abSerial

- Please see *abSerial Reference Guide* for details



## 2 Hardware Specifications

General Conditions ( $V_{IN}$ = 3.0V and 25°C)

### 2.1 Recommended Operating Conditions

Rating	Min	Typical	Max	Unit
Operating Temperature Range	-40	-	85	°C
Supply Voltage $V_{IN}$	2.1	2.5	3.6	Volts
Signal Pin Voltage	-	2.1	-	Volts
RF Frequency	2400	-	2483.5	MHz

### 2.2 Current Consumption

High speed CPU mode, 32 MHz <ul style="list-style-type: none"> <li>▪ UART supports up to 921 Kbps</li> <li>▪ Data throughput up to 1.0 Mbps</li> <li>▪ abSerial v1.4 (firmware)</li> <li>▪ Shallow Sleep enabled</li> </ul>		
Modes (Typical Power Consumption)	Avg	Unit
ACL data 115K Baud UART at max throughput (Master)	23.0	mA
ACL data 115K Baud UART at max throughput (Slave)	27.5	mA
Connection, no data traffic, master	9.1	mA
Connection, no data traffic, slave	11.2	mA
Connection, 375ms sniff, slave (external LPO required)	490	mA
Standby, without deep sleep	8.6	mA
Standby, with deep sleep, no external LPO	1.7	mA
Standby, with deep sleep, with external LPO	70	uA
Page/Inquiry Scan, with deep sleep, no external LPO	1.7	mA
Page/Inquiry Scan, with deep sleep, with external LPO	520	uA
Bluetooth power down / CPU standby	15	μA

Standard CPU Mode, 8 MHz <ul style="list-style-type: none"> <li>▪ UART supports up to 115 Kbps</li> <li>▪ Data throughput up to 200 Kbps</li> <li>▪ abSerial v1.4 (firmware)</li> <li>▪ Shallow Sleep enabled</li> </ul>		
Modes (Typical Power Consumption)	Avg	Unit
ACL data 115K Baud UART at max throughput (Master)	16.7	mA
ACL data 115K Baud UART at max throughput (Slave)	18.0	mA
Connection, no data traffic, master	4.9	mA
Connection, no data traffic, slave	7.0	mA
Connection, 375ms sniff, slave (external LPO required)	490	mA
Standby, without deep sleep	4.2	mA
Standby, with deep sleep, no external LPO	1.7	mA
Standby, with deep sleep, with external LPO	70	uA
Page/Inquiry Scan, with deep sleep, no external LPO	1.6	mA
Page/Inquiry Scan, with deep sleep, with external LPO	520	uA
Bluetooth power down / CPU standby	15	μA

### 2.3 Selected RF Characteristics

Parameters	Conditions	BT Spec	Typical	Unit
Antenna load			50	ohm
<b>Radio Receiver</b>				
Sensitivity level	BER < .001 with DH5	≤ -70	-88	dBm
Maximum usable level	BER < .001 with DH1	≥ -20	0	dBm
Input VSWR			2.5:1	
<b>Radio Transmitter</b>				
Maximum output power	50 Ω load	-6 to +4	+8	dBm
Initial Carrier Frequency Tolerance		± 75	0	kHz
20 dB Bandwidth for modulated carrier		≤ 1000	935	kHz

### 2.4 Absolute Maximum Ratings

Rating	Min	Typical	Max	Unit
Storage temperature range	-55	-	+150	°C
Supply voltage $V_{IN}$	-0.3	-	+5.0	Volts
I/O pin voltage $V_{IO}$	-0.3	-	+5.5	Volts
RF input power	-	-	-5	dBm

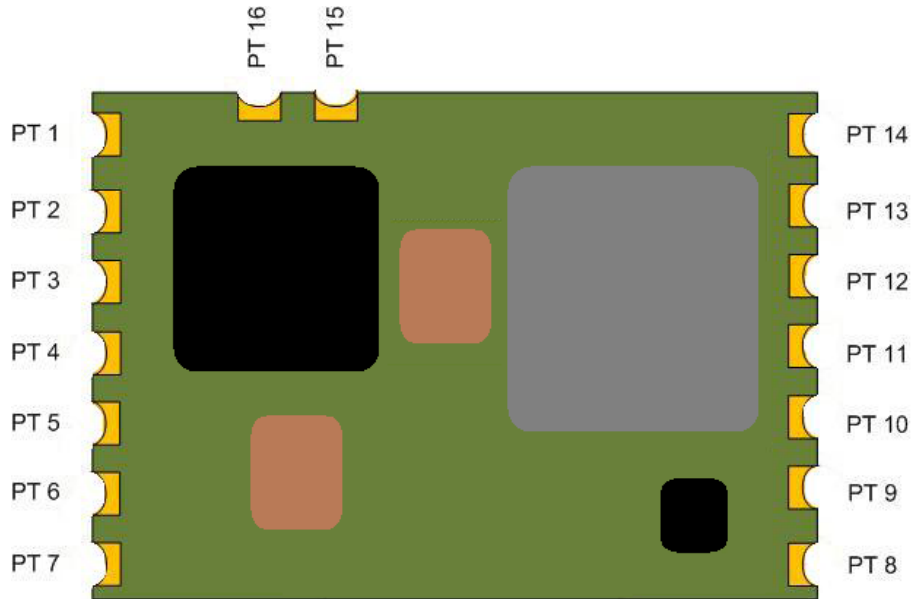
### 2.5 I/O Operating Characteristics

Symbol	Parameter	Min	Max	Unit	Conditions
$V_{IL}$	Low-Level Input Voltage	-	0.6	Volts	$V_{IN}$ , 2.1V
$V_{IH}$	High-Level Input Voltage	1.4	-	Volts	$V_{IN}$ , 2.1V
$V_{OL}$	Low-Level Output Voltage	-	0.4	Volts	$V_{IN}$ , 2.1V
$V_{OH}$	High-Level Output Voltage	1.8	-	Volts	$V_{IN}$ , 2.1V
$I_{OL}$	Low -Level Output Current	-	4.0	Ma	$V_{OL} = 0.4 V$
$I_{OH}$	High-Level Output Current	-	4.0	Ma	$V_{OH} = 1.8 V$
$R_{PU}$	Pull-up Resistor	80	120	KΩ	Resistor Turned On
$R_{PD}$	Pull-down Resistor	80	120	KΩ	Resistor Turned On

## 2.6 Pin Assignment

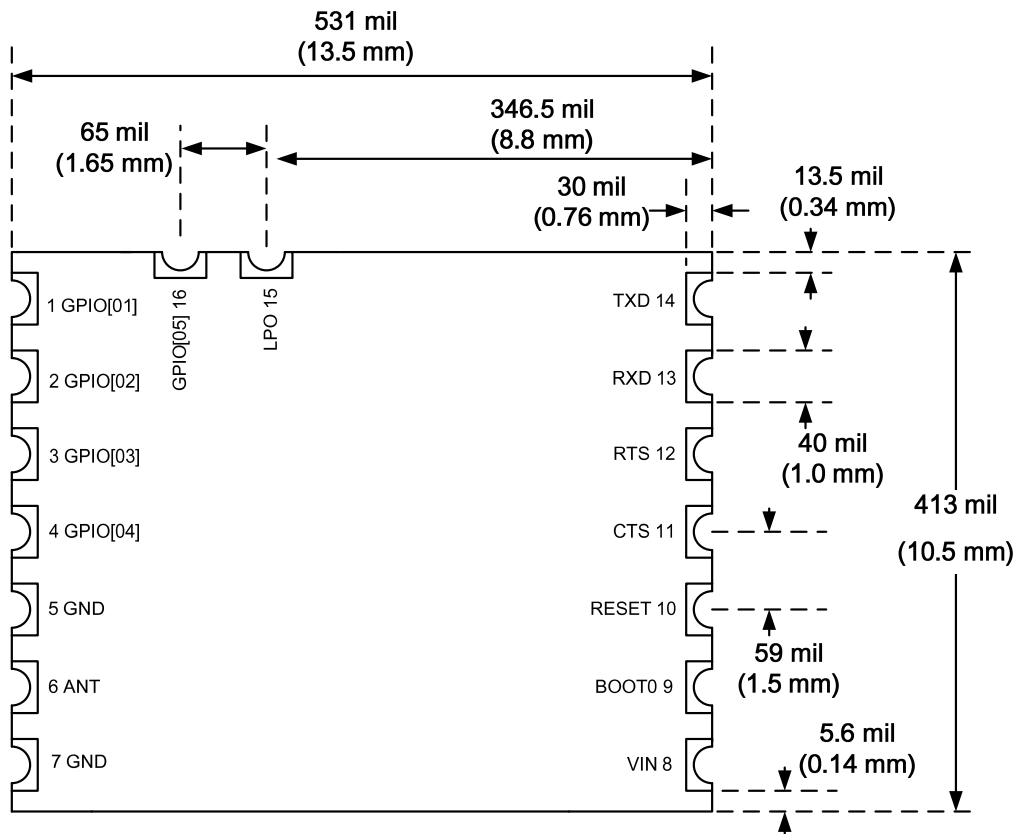
Name	Type	Pin #	Description	ALT Function	5V Tolerant	Initial State
<b>UART Interface</b>						
RXD	I	13	Receive data		Y	
TXD	O	14	Transmit data		Y	
RTS	O	12	Request to send (active low)	I2C SDA/UART RX	Y	
CTS	I	11	Clear to send (active low)	I2C SCL/UART TX	Y	
<b>Antenna</b>						
ANT	I/O	6	50Ω Rx/Tx antenna port			
GND		5	GND			
<b>Reserved</b>						
BOOT 0	I	9	Reserved			
<b>Power and Ground</b>						
VIN		8	VIN			
GND		7	GND			
<b>Reset</b>						
RESETN	I	10	Reset input (active low for 5 ms);		2.5V max	
<b>LPO</b>						
LPO	I	15	LPO input			
<b>GPIO – General Purpose Input/Output</b>						
GPIO [1]	I/O	1	General Purpose Input/Output	SPI MISO	Y	Input pull down
GPIO [2]	I/O	2	General Purpose Input/Output	SPI MOSI/I2S_SD	Y	Floating
GPIO [3]	I/O	3	General Purpose Input/Output	SPI SCLK/ I2S_CK	Y	Input pull down
GPIO [4]	I/O	4	General Purpose Input/Output	SPI SS/I2S_WS	Y	Input pull down
GPIO [5]	I/O	16	General Purpose Input/Output	DAC 0/ADC 0	2.5V max	Input pull down

## 2.7 Pin Placement Diagram (Top View)



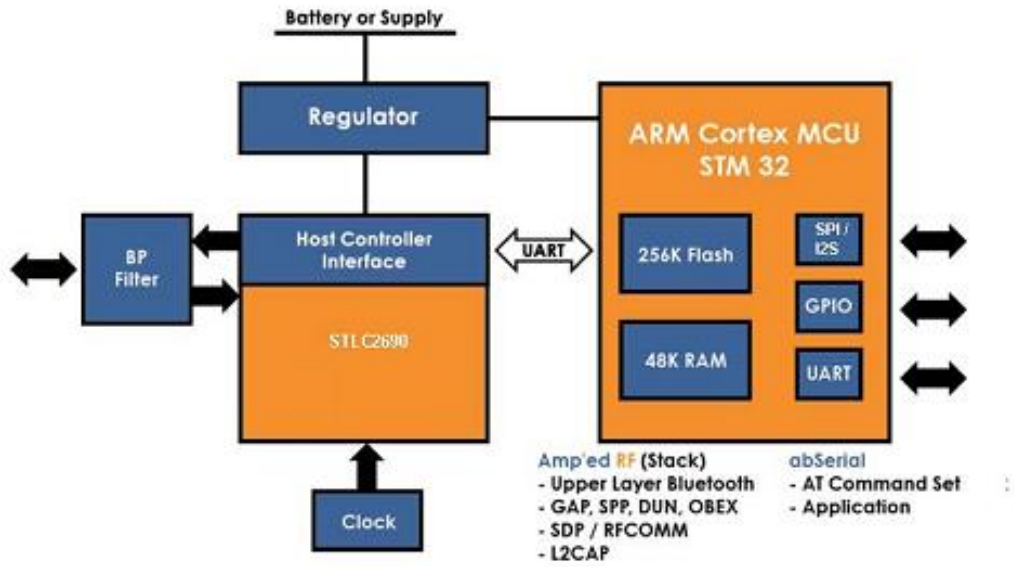
## 2.8 Layout Drawing

Size: 10.5 mm x 13.5 mm x 2.2 mm





### 3 Hardware Block Diagram



## 4 Hardware Design

Amp'ed RF modules support UART, USB, SPI, and GPIO hardware interfaces. Please note that the usage of these interfaces is dependent upon the firmware that is loaded into the module, and is beyond the scope of this document. The AT command interface uses the main UART by default.

### Notes

- All unused pins should be left floating; do not ground.
- All GND pins must be well grounded.
- The area around the antenna should be free of any ground planes, power planes, trace routings, or metal for at least 5 mm in all directions.
- Traces should not be routed underneath the module.

### 4.1 Module Reflow Installation

The BT32 is a surface mount Bluetooth module supplied on a 16 pin, 6-layer PCB. The final assembly recommended reflow profiles are:

For RoHS/Pb-free applications, Sn96.5/Ag3.0/Cu0.5 solder is recommended.

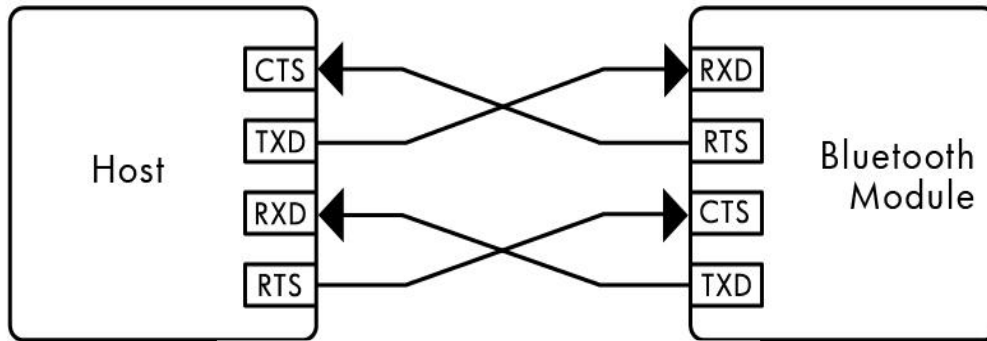
- Maximum peak temperature of 230° - 240°C (below 250°C).
- Maximum rise and fall slope after liquidous of < 2°C/second.
- Maximum rise and fall slope after liquidous of < 3°C/second.
- Maximum time at liquidous of 40 – 80 seconds.

### 4.2 GPIO Interface

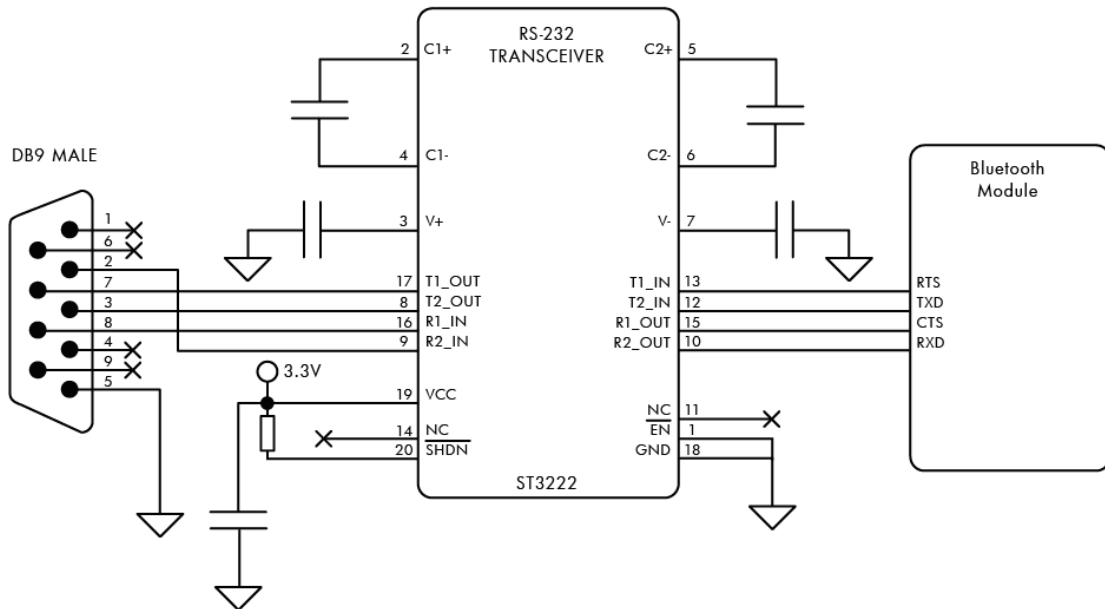
All GPIOs are capable of sinking and sourcing 4mA of I/O current.

### 4.3 UART Interface

The UART is compatible with the 16550 industry standard. Four signals are provided with the UART interface. The TXD and RXD pins are used for data while the CTS and RTS pins are used for flow control.



Connection to Host Device

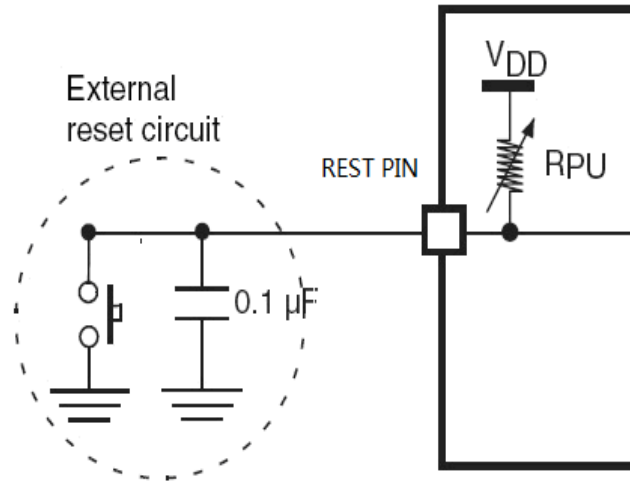


Typical RS232 Circuit

#### 4.4 Reset Circuit

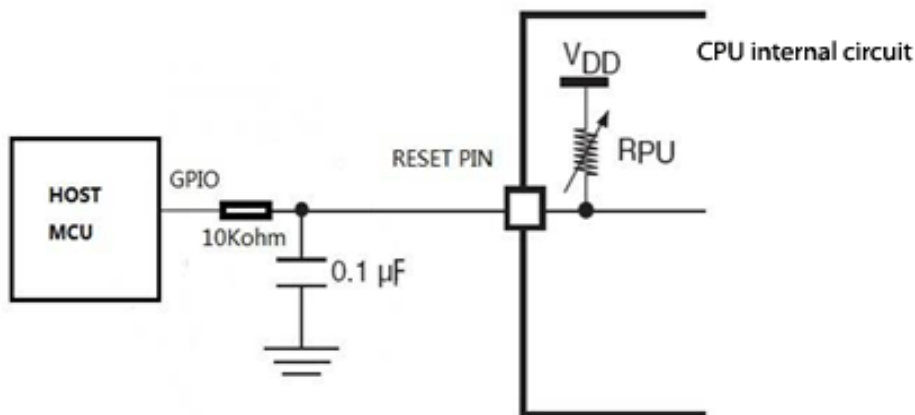
Two types of system reset circuits are detailed below.

##### 4.4.1 External Reset Circuit:



Note:  $R_{PU}$  ranges from 30K ohm to 50K ohm internally.

##### 4.4.2 Internal Reset Circuit:



Notes:

- $R_{PU}$  ranges from 30K ohm to 50K ohm internally.
- $R_{RST}$  should be from 1K ohm to 10K ohm

### 4.5 External LPO Input Circuit

An optional low power oscillator input may be added to allow deep sleep and sniff modes.

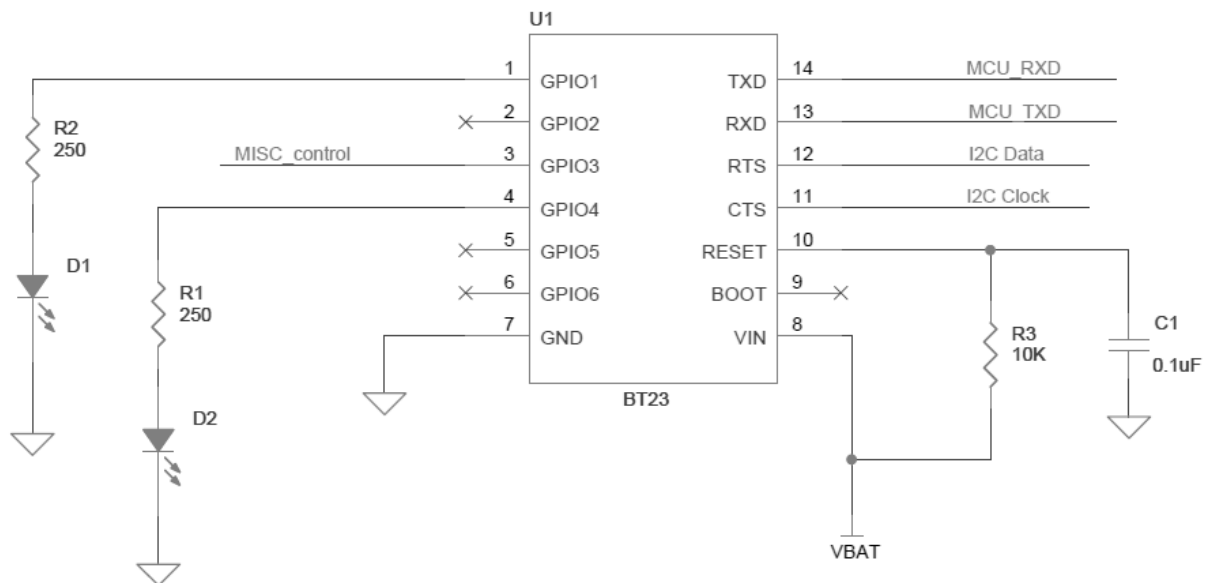
#### LPO Parameters:

- Frequency: 32.768 KHz
- Tolerance: 150 ppm
- Voltage Levels
- Low: 0.9 V
- High: 1.8 V
- Input Capacitance: 2.5 pF maximum

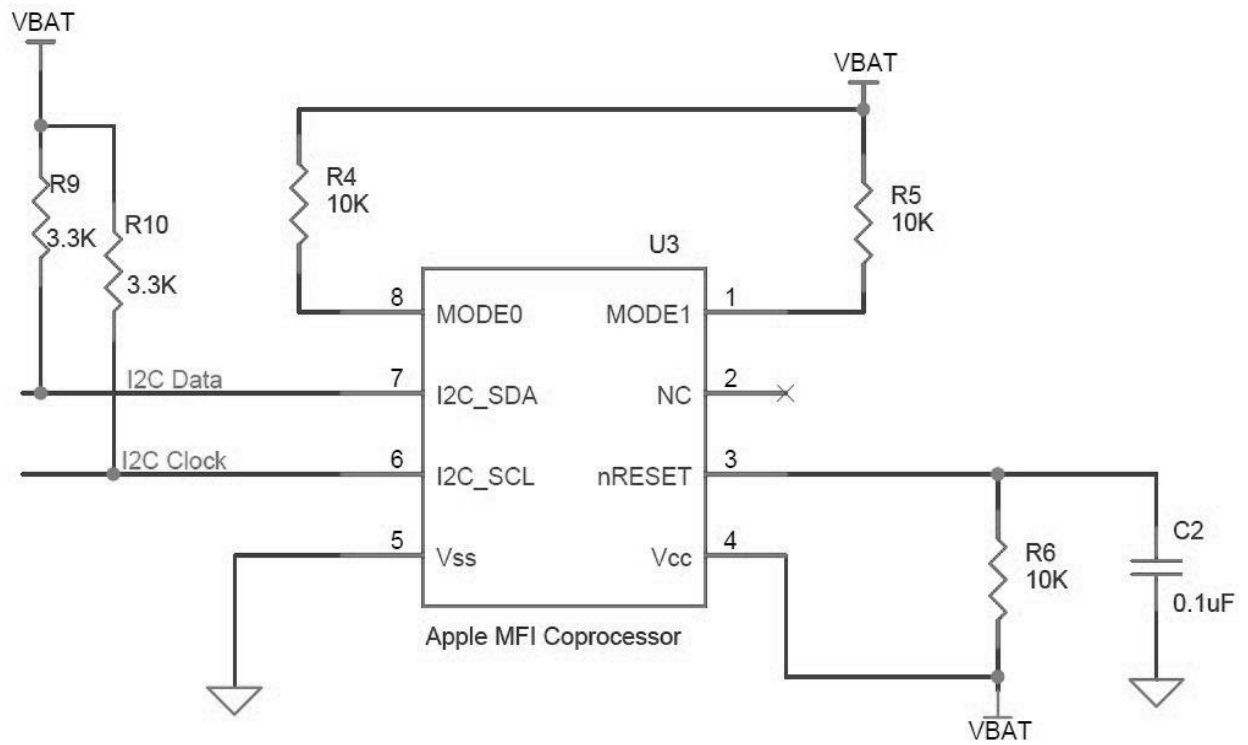
#### Configurations:

- See configuration guide:
- UseExtLPO
- AllowSniff

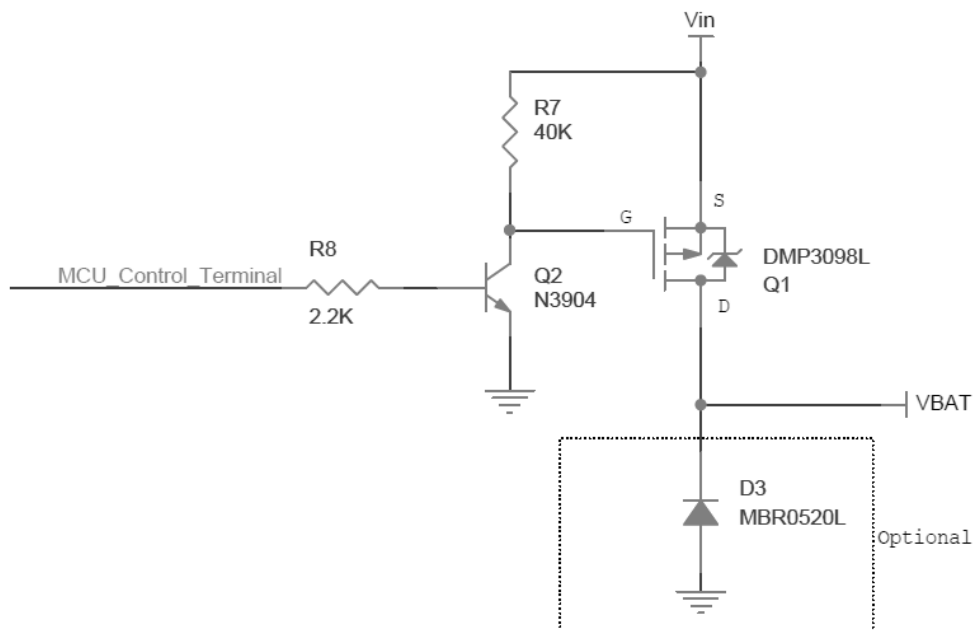
### 4.6 Apple iOS CP Reference Design



Part 1. BT module



**Part 2. Co-processor**



**Part 3. Power switch**

## 5 FCC Regulatory Compliance

This module has been tested and found to comply with the FCC Part15 and IC RSS-210 rules. These limits are designed to provide reasonable protection against harmful interference in approved installations. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Modifications or changes to this equipment not expressly approved by Amp'ed RF Technology may void the user's authority to operate this equipment.

## Ordering Information

Part Name	Description
BT32A	Standard version
BT32LT	Lite version, limited features

## 6 Feature Comparison

Features	BT32A	BT32LT
CPU Speed	72MHz Max.	36MHz Max.
CPU Memory	256K Flash, 48K RAM	128K Flash, 16K RAM
Bluetooth Profile Support	SPP, IAP, OBEX, DUN	SPP and IAP
Bluetooth Stack	Amp'ed UP, BT v3.0	Amp'ed UP, BT v3.0
Apple iOS Support	Supported	Supported
AT Command Interface	abSerial	abSerial Lite
Multiple Connections	7 Max.	1 Max.
Link Throughput	1.5M bps Max.	300K bps Max.
Serial Interface	UART, I2C, SPI	UART, I2C, SPI
General I/O Lines	5	5
A/D Lines	1	1
DAC Lines	1	0
Shield	No	No

## 7 Revision History

Date	Revision	Description
09-May-2011	1	First release
27-Feb-2013	1.1	Added BT32LT information
15-July-2013	1.2	Updated dimensions and drawing